# PC915

### Features

- 1. Wide band linear output type (Frequency band width : TYP. 10Hz to 8MHz)
- 2. Fluctuation free stable output
  (Output fluctuation : TYP. ± 5% at within operating temperature 50 000hr )
- 3. High isolation voltage

(  $V_{\mbox{\tiny iso}}$  : 5 000V  $_{\mbox{\tiny rms}}$  )

- 4. Standard dual-in-line package
- 5. Recognized by UL, file No, E64380

### Applications

- 1. Video signal insulation in TV
- 2. Insulation amplifier in measuring instrument and FA equipment

# Wide Band Linear Output Type OPIC Photocoupler

### Outline Dimensions



\* "OPIC " ( Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and signalprocessing circuit integrated onto a single chip.

### Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$ 

	Parameter	Symbol	Rating	Unit
Input	Forward current	I <sub>F</sub>	25	mA
	Reverse voltage	VR	6	V
	Power dissipation	Р	45	mW
	Supply voltage	V <sub>CC</sub>	- 0.5 to + 13	V
Output	Output power dissipation	Po	250	mW
	Output current	Io	- 1.0 to + 0.5	mA
	*1Isolation voltage	V iso	5 000	V rms
	Operating temperature	T opr	- 25 to + 85	°C
	Storage temperature	T stg	- 55 to + 125	°C
	*2Soldering temperature	T sol	260	°C

\*1 40 to 60% RH, AC for 1 minute

\*2 For 10 seconds

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Electro-optical Characteristics

Elect	ro-optica	al Characteristi	cs		(Unless of	therwise	spcified	d, $Ta = 2$	25°C)
		Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit	Fig.
Input	Forward voltage		VF	$I_F = 10 mA$	-	1.6	1.8	V	1
	Reverse voltage		IR	$V_R = 5V$	-	-	10	μΑ	-
	Terminal capacitance		Ct	V = 0, f = 1MHz	-	60	250	pF	-
Output	Supply current		Icc	$I_F = 10 mA$	-	9	16	mA	1
	DC output voltage		V ODC	$I_F = 10 mA$	4	6	8	V	1
	Output noise voltage		V ono	$I_F = 10mA$ , Band width= 100Hz to 4.2MHz	-	4	-	mV <sub>rms</sub>	1
Transfer charac- teristics	AC output voltage		V OAC	$R_E = 230 \Omega$	0.8	1.0	1.2	V P-P	2
	AC output voltage	*1 Temperature characteristics	$\Delta V_{OAC-1}$	$R_{E} = 230 \Omega,$ Ta = 10 to 70°C	-	± 3	-	%	2
	fluctuation	*2 Forward current characteristics	$\Delta V_{OAC-2}$	$R_E = 230$ to $460 \Omega$	-	± 3	-	%	2
	*3 Cout off	High frequency	f <sub>CH</sub>	$R_E = 230 \Omega$	6	8	-	MHz	2
	frequency	Low frequency	$f_{CL}$	$R_E = 230 \Omega$	-	10	20	Hz	2
	Differential gain		DG		-	+ 3	-	%	3
	Differential phase		DP		-	- 3	-	0	3
	Isolation resistance		R <sub>ISO</sub>	DC500V, 40 to 60% RH	5 x 10 <sup>10</sup>	1 x 10 <sup>11</sup>	-	Ω	-
	Floating capacitance		Cf	V = 0, $f = 1$ MHz	-	0.6	5	pF	-

\*1 Fluctuation ratio of  $V_{OAC}$  at Ta = - 10 to 70  $^{\circ}C$  on the basis of  $V_{OAC}$  at Ta = 25  $^{\circ}C$ \*2 Fluctuation ratio of  $V_{OAC}$  at  $R_E$  = 230 to 460  $\Omega$  on the basis of  $V_{OAC}$  at  $R_E$  = 230  $\Omega$ 

\*3 Frequency of  $V_{IN}$  when  $V_{OAC}$  falls by 3dB on the basis of  $V_{OAC}$  when frequency of  $V_{IN}$  in Fig. 2 is 100kHz.

#### Recommended Operating Conditions

Parameter		Symbol	MIN.	MAX.	Unit
Input	Forward bias current	$I_{FB}$	8	15	mA
Output	Supply voltage	V <sub>CC</sub>	8	13	V
	AC output voltage	V OAC	-	4	V <sub>P-P</sub>
	Output current	Io	- 0.6	+ 0.2	mA
	C terminal capacitance	Cc	10	-	μF

# Test Circuit

Fig.1



#### Fig. 2



### V<sub>IN</sub> Waveform



(Frequency) 15kHz at measuring V  $_{OAC},~\Delta$  V  $_{OAC-1}$  and  $~\Delta$  V  $_{OAC-2}$  and shall be swept at measuring f  $_{CH}$  and f  $_{CL}.$ 

Fig. 3











Fig. 5 Power Dissipation vs. Ambient Temperature







### **Test Circuit of Supply Current**



### Fig. 6 Forward Current vs. Forward Voltage



Fig. 8-a Relative AC Output Voltage 1 vs. Ambient Temperature



# Test Circuit of Relative AC Output Voltage1 vs. Ambient Temperatue









### Fig. 9 Differential Gain vs. R E



# Test Circuit of Relative AC Output Voltage 2 vs. Freguency (1)



# Test Circuit of Relative AC Output Voltage 2 vs. Freguency (2)



### Fig.10 Differential Phase vs. R E



### Test Circuit of Differential Gain vs. R $_{\text{E}}$ and Differential Phase vs. R $_{\text{E}}$



## Application Example





APL: Average Picture Level





 $V_E$  : Emitter voltage of  $T_{r2}$  (Between emitter and GND)

# < Example of Circuit Setting >

(1) Set for Gain

Gain is represented by the following formula ;

 $G = 2.3/(V_{\rm CC} - V_{\rm E})$ 

When using on condition that Gain = 1, set  $V_{CC} - V_E$  on 2.3V. So that  $R_1$  and  $R_2$  is determined.

(2) Set for Input Resistance

Set Ri on output impedance (usually  $75\Omega$ ) of a mounting equipment.

(3) Set for R  $_{\rm E}$ 

When there is no signal (input signal : 0), set I  $_{\text{LED}}$  flowed into infrared LED on 10 mA.

(4) Set for Low Cut-off Frequency

Low cut-off frequency with C terminal capacitance, C  $_{\rm C}$  , is represented by the following formula;

f c= 100/C c(Hz)(Cc :  $\mu$  F value )

Then set Ci with input impedance of by-pass diode on as much value as possible on condition that  $f_c>1/(2\pi \text{ CiR})[R=R_1R_2/(R_1+R_2)]$ 

### Precautions for Use

- (1) It is recommended that a by-pass capacitor of more than 0.01  $\mu$  F is added between  $V_{\rm cc}$  and GND near the device in order to stabilize power supply line.
- (2) Handle this product the same as with other integrated circuits against static electricity.
- (3) As for other general cautions, refer to the chapter "Precautions for Use"

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